

AMENDMENTS TO THE CLAIMS

1. **(Currently Amended)** A semiconductor integrated circuit device having a second storage means in a semiconductor integrated circuit, in which a program that makes an arithmetic processing unit in the semiconductor integrated circuit perform an operation of processing contents is rewritably stored, and performing rewriting of the program stored in the second storage means using a first storage means in which a rewrite program for rewriting is stored, which rewrite program makes the arithmetic processing unit perform an operation of processing the contents;

wherein said second storage means has an externally readable area that can be read from the outside of the semiconductor integrated circuit, and an externally unreadable area that cannot be read from the outside; and

after arbitrary data is stored in the externally readable area of the second storage means, the data is read to the outside of the semiconductor integrated circuit to check whether the arbitrary data is ~~the data as inputted~~ correct or not, and thereafter, the rewrite program read from the first storage means is stored in the externally unreadable area of the second storage means.

2. **(Currently Amended)** A semiconductor integrated circuit device having a second storage means in a semiconductor integrated circuit, in which a program that makes an arithmetic processing unit in the semiconductor integrated circuit perform an operation of processing contents is rewritably stored, and performing rewriting of the program stored in the second storage means using a first storage means in which a rewrite program for rewriting is stored, which rewrite program makes the arithmetic processing unit perform an operation of processing the contents, said semiconductor integrated circuit device including:

a control circuit for performing control so as to read only a specific portion of the rewrite program stored in the second storage means which is used for judging whether the rewrite program is correct or not.

3. **(Original)** A semiconductor integrated circuit device as defined in Claim 2 wherein said control circuit performs control so as to read only the rewrite program located in specific addresses of the second storage means.

4. **(Original)** A semiconductor integrated circuit device as defined in Claim 2 wherein said control circuit performs control so as to read only specific bits of the rewrite program stored in the second storage means.

5. **(Currently Amended)** A semiconductor integrated circuit device having a second storage means in a semiconductor integrated circuit, in which a program that makes an arithmetic processing unit in the semiconductor integrated circuit perform an operation of processing contents is rewritably stored, and performing rewriting of the program stored in the second storage means using a first storage means in which a rewrite program for rewriting is stored, which rewrite program makes the arithmetic processing unit perform an operation of processing the contents;

wherein said rewrite program includes a program for executing a portion of the rewrite program after the rewriting to judge whether the rewrite program is correct or not; and the portion of the rewrite program stored in the second storage means is executed.

6. **(Original)** A semiconductor integrated circuit device as defined in Claim 5 wherein the portion of the rewrite program to be executed is one for successively executing discontinuous program areas.

7. **(Currently Amended)** A semiconductor integrated circuit device having a second storage means in a semiconductor integrated circuit, in which a program that makes an arithmetic processing unit in the semiconductor integrated circuit perform an operation of processing contents is rewritably stored, and performing rewriting of the program stored in the second storage means using a first storage means in which a rewrite program for rewriting is stored,

which rewrite program makes the arithmetic processing unit perform an operation of processing the contents; and

said semiconductor integrated circuit device including, in the semiconductor integrated circuit, a transfer monitor means for monitoring transfer errors of the rewrite program to be transferred from the first storage means to the second storage means to judge whether the rewrite program is correct or not.

8. **(Original)** A semiconductor integrated circuit device having a second storage means in a semiconductor integrated circuit, in which a program that makes an arithmetic processing unit in the semiconductor integrated circuit perform an operation of processing contents is rewritably stored, and performing rewriting of the program stored in the second storage means using a first storage means in which a rewrite program for rewriting is stored, which rewrite program makes the arithmetic processing unit perform an operation of processing the contents;

wherein the rewrite program includes a check program for checking whether the program is correct or not;

the semiconductor integrated circuit is provided with a work memory for the arithmetic processing unit, and a connection switching means for switching the connection between the second storage means or the work memory, and the program input or the data input of the arithmetic processing unit; and

the check program that is extracted from the rewrite program stored in the second storage means is stored in the work memory, and the arithmetic processing unit is operated by the check program stored in the work memory, thereby to check whether the rewrite program is correct or not.

9. **(Original)** A semiconductor integrated circuit device as defined in Claim 8 wherein the second storage means holds the rewrite program, and holds data which is uniquely obtained from a predetermined cluster in the rewrite program, according to a predetermined rule.

10. **(Original)** A semiconductor integrated circuit device as defined in Claim 9 wherein the uniquely obtained data is used as a check code for checking whether the program is correct or not.

11. **(Original)** A semiconductor integrated circuit device as defined in Claim 8 wherein the second storage means has a construction in which an area where the rewrite program is not stored is successively divided into two areas, and the same program is stored in each of the two areas; the check program includes

a program for comparing the program data stored in one of the two areas with the same data stored in the other area, thereby to check whether the program data is correct or not, and

a program for, when the result of the previous check is that the program data is correct, repeating an operation of further dividing one of the two areas, as an area wherein no program is stored, into two areas, and storing the same program data in each of the two areas; and

all of the programs to be stored in the second storage means are successively stored.

12. **(Original)** A semiconductor integrated circuit device as defined in Claim 11 wherein the second storage means stores the rewrite program data, and data that is uniquely obtained from the program data according to a predetermined rule, in the two areas into which the area in the second storage means where the rewrite program is not stored is successively divided.

13. **(Original)** A semiconductor integrated circuit device as defined in Claim 12 wherein the uniquely obtained data is inverted data of the program data.

14. **(Previously Presented)** A semiconductor integrated circuit device as defined in Claim 8 further including a ROM (Read Only Memory) in which the check program is previously stored;

wherein the arithmetic processing unit is operated by the ROM to check whether the rewrite program is correct or not.

15. **(Previously Presented)** A semiconductor integrated circuit device as defined in Claim 1 further including, in the semiconductor integrated circuit, a decryption means for decrypting the encrypted rewrite program;

wherein, when the rewrite program stored in the first storage means has previously been encrypted, the decryption means decrypts the encrypted program, and stores the decrypted rewrite program in the second storage means.

16. **(Currently Amended)** A semiconductor integrated circuit device having a second storage means in a semiconductor integrated circuit, in which a program that makes an arithmetic processing unit in the semiconductor integrated circuit perform an operation of processing contents is rewritably stored, and performing rewriting using a first storage means in which a previously encrypted rewrite program for rewriting is stored, which rewrite program makes the arithmetic processing unit perform an operation of processing the contents;

said semiconductor integrated circuit device including, in the semiconductor integrated circuit,

a decryption means for decrypting the encrypted rewrite program read from the first storage means, and transferring the decrypted rewrite program to the second storage means; and

an encryption means for again encrypting the rewrite program stored in the second storage means;

wherein the rewrite program encrypted by the encryption means is compared with the encrypted rewrite program stored in the first storage means to judge whether the rewrite program is correct or not.

17. **(Previously Presented)** A semiconductor integrated circuit device as defined in Claim 11 wherein, when data are not correctly stored in the second storage means, a defective portion is detected, and the rewrite program stored in the first storage means is corrected.

18. **(Previously Presented)** A semiconductor integrated circuit device as defined in Claim 1 wherein the rewrite program that is stored outside the semiconductor integrated circuit device is downloadable into the semiconductor integrated circuit.

19-50. **(Canceled)**